



APPLICANT: Ted Humphrey

TITLE: Analog Electronic Device

APPLICATION #: 10/627,670

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Examiner: Singleton, Michael B.

Art Unit: 2817

This is a response to US PTO Office Action of Oct 22, 2004 with a mailing date of 11/05/2004.

Applicant hereby elects Species III as detailed in said Office Action.

Species III is directed toward and includes Figures 13, 14 and 19 per Office Action.

The following claims are supported by and embodied in Figures 13, 14 and 19:

Claim 1: invention is used twice in figure 13, twice in figure 14 and four (4) times in figure 19,

Claim 2: invention is used twice in figure 13, twice in figure 14 and four (4) times in figure 19,

Claim 4: where invention as claimed is used two (2) times in figure 13,

Claim 6: where invention as claimed is used two (2) times each in figures 13, 14 and 19,

Claim 7: where invention as claimed is used once in each of figures 13, 14 and 19,

Claim 8: where invention as claimed is used once in figure 13,

Claim 9: where invention as claimed is used once each in figures 14 and 19,

Claim 10: where invention as claimed is used once each in figures 14 and 19,

Claim 11: where invention as claimed is used two (2) times in figure 19,

Claim 12: where invention as claimed is used once in figure 19,

Claim 13: where invention as claimed is used once in figure 19, and

Claim 20: where invention as claimed is used twice in figure 13, twice in figure 14 and four (4) times in figure 19.

NB: To make examination of the drawings easier, certain rules were followed. These are detailed in the first paragraph of section ***SYMBOLS and EQUATIONS***. In essence, any transistors labeled with subscripts in the range of 1-8, refer back to embodiments as expressed in figures 8a-8d. This makes it easier to spot exactly how/where the invention is used in the figures.

Ted Humphrey